

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10550326
Filing Date	2005-09-23
First Named Inventor	Roger D. Chamberlain
Art Unit	2131
Examiner Name	
Attorney Docket Number	53047-57365

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	1	"Lucent Technologies Delivers "Payload Plus" Network Processors for Programmable, Multi-Protocol, OC-48c Processing", Lucent Technologies Press Release, downloaded from http://www.lucent.com/press/1000/0010320.meb.html on March 21, 2002	<input type="checkbox"/>

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	2	"Overview, Field Programmable Port Extender", January 2002 Gigabit Workshop Tutorial, Washington University, St. Louis, MO, January 3-4, 2002	<input type="checkbox"/>
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	24	International Search Report for PCT/US2001/011255; July 10, 2003	<input type="checkbox"/>
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